



Fee only

JTW

Docket No.: GR 98 P 1916 P

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By: 

Date: October 19, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/740,634 Confirmation No: 4357
Applicant : Wilhelm Schmid et al.
Filed : December 18, 2000
Title : Device for Measurement and Analysis of Electrical Signals of an Integrated Circuit Component
Art Unit : 2829
Examiner : Paresh H. Patel

Docket No. : GR 98 P 1916 P
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(i)

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Patent Abstracts of Japan JP 61 228 365 A (Yoshiki et al.), dated October 11, 1986;

01/14/2005 Patent Abstracts of Japan JP 04 076 474 A (Imamura), dated March 11, 1992;

01 FC:1806 180.00 DA

German translation of Japanese Office Action dated June 17, 2004.